



EVAL-AD7616SDZ (Rev. D) (08-039639) - Component Side View
Layer 1 - Component Side

Drill Template

1. Material: Four layer, FR4 glass epoxy laminate, 0.062" +/- .007" thick.
1 oz. copper clad - external layers. 1 oz. copper clad - internal layers.
Finished Board to be RoHS & REACH Compliant.
2. Plated thru holes and the conductive pattern electroplated with 0.001" min. thick copper. Terminal areas and plated thru holes to be ENIG plated.
Soldermask over bare copper.
3. All Boards to be Electrically Tested prior to shipment
4. Datum for (x,y) co-ordinate drill files at LLH corner.
5. Processing tolerances:
 - A. Conductive pattern front to back registration within .005" total.
 - B. Minimum annular ring surrounding holes: 0.002"
 - C. Finished conductive pattern within .002" of true size.
6. Warp and twist within .010 inch per inch
7. Dimensions are for the finished part.
8. Solder Mask: Liquid photo imageable solder mask over bare copper (smabc), colour green, both sides using the patterns provided. No mask is permitted on the terminal areas. Soldermask to etch registration within .005" total.
9. Screening: Screen component outlines and nomenclature using indelible white ink on the primary and secondary sides. Nomenclature shall be legible.
Screen to etch registration within .020" total.
10. Surfaces: Punched or machined surfaces 125 micro inches rms max.
11. Break all sharp edges .015" R max.
12. Manufacturer to add UL Vendor ID Number , Date Code (YY/WW Format) in this area on the component side silkscreen.

SIZE	QTY	SYM	PLATED	TOL
0.889	66	⊗	YES	+/-2.0
1.0414	30	⊕ ^L	YES	+/-2.0
1.524	153	⊕ ^M	YES	+/-0.0
1.1	1	⊕ ^N	NO	+/-0.0
0.7	1	⊕ ^O	NO	+/-0.0
3.2	2	⊕ ^P	NO	+/-0.0
1	8	⊕ ^Q	YES	+/-0.0
0.9398	35	⊕ ^R	YES	+/-0.0
4.4958	4	⊕ ^S	NO	+/-0.0
1.8	1	⊕ ^T	NO	+/-0.0
1.6	1	⊕ ^U	NO	+/-0.0
0.25	202	⊕ ^V	YES	+/-0.0
0.6	10	⊕ ^W	YES	+/-0.0